

01-13-04

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Yao-Sheng Lee

SERIAL NUMBER: 10/616,492

FILING DATE: 07/08/03

ART UNIT: 1763

FOR: High Temperature Anisotropic Etching of Multi-Layer

ATTORNEY DOCKET NO.: 074361.00015

U.S. Patent and Trademark Office  
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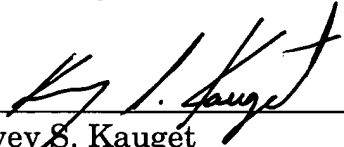
As listed on the attached Form PTO-1449, Applicant submits herewith copies of the patents, publications or other information of which he is aware, which he believes may be material to the examination of this application and in respect of which there may be a duty to disclose.

The filing of this information disclosure statement shall not be construed as a representation that a search has been made (37 CFR 1.97(g)), an admission that the information cited is, or is considered to be, material to patentability or that no other material information exists.

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The information disclosure statement submitted herewith is being filed within three months of the filing date of the application or date of entry into the national stage of an international application or before the mailing date of a first Office Action on the merits, whichever event occurs last.

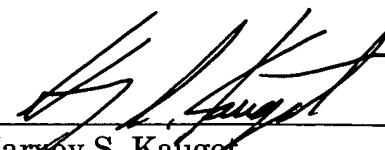
Respectfully submitted,

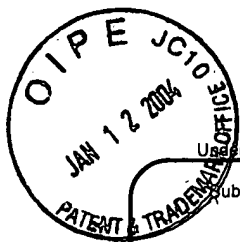
  
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CERTIFICATE OF EXPRESS MAILING

I HEREBY CERTIFY that the foregoing was placed in an envelope and mailed via express mail, (ER337228602) postage paid, to the U.S. Patent and Trademark Office, 2011 South Clark Place, Customer Window, Mail Stop DD, Crystal Plaza Two, Lobby, Room 1B03, Arlington, VA 22202 on this the 12<sup>th</sup> day of January, 2004.

The Commissioner is hereby authorized to charge any additional fees which may be required at any time during the prosecution of this application without specific authorization, or credit any overpayment, to Deposit Account Number 50-1667.

  
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STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 2

Application Number	10/616,492
Filing Date	07/08/2003
First Named Inventor	Yao-Sheng Lee
Art Unit	1763
Examiner Name	
Attorney Docket Number	074361.00015

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US- 6,350,390	02-26-2002	Liu et al.	
		US- 6,514,378	02-04-2003	Ni et al.	
		US- 5,545,290	08-13-1996	Douglas	
		US- 2002/0037647	03-28-2002	Hwang et al.	
		US- 2003/0059720	03-27-2003	Hwang et al.	
		US- 6,323,132	11-27-2001	Hwang et al.	
		US- 5,419,804	05-30-1995	Ojha et al.	
		US- 5,869,398	02-09-1999	Knight	
		US- 5,968,845	10-19-1999	Chino et al.	
		US- 6,127,201	10-03-2000	Chino et al.	
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
		JP 09050983	02-18-1997	Fujitsu Ltd.		
		JP 09162164	06-20-1997	Fujitsu Ltd.		
		JP 404294533	10-19-1992	Nakamura, Tomoko		
		JP 403141640	06-17-1991	Tatsumi, Tetsuya		

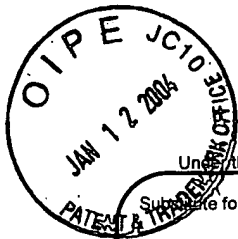
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Attorney Docket Number	074361.00015

Sheet	2	of	2
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**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		S. OKU, Y. SHIBATA, and K. OCHIAI, "Controlled Beam Dry Etching of InP by Using Br <sub>2</sub> -N <sub>2</sub> Gas," J. Electron Mater., Vol 25, pp. 585-591, 1996.	
		D.J. THOMAS K. POWELL, M.M. BOURKE, Y.P. SONG and C. FRAGOS, "High Density Plasma Etch Processes for the Manufacture of Optoelectronic Devices based on InP", GaAs MANTECH Digest of Papers - 2001, pp 172-174	
		D. LISHAN, J. LEE, and G. KIM, "InP Processing Using an HBr High Density ICP Plasma", GaAs MANTECH Digest of Papers - 2001, pp 175-178	
		S. AGARWALA, K. NUMMILA, H., I. ADESIDA, C. CANEAU, AND R. BHAT, "InAlAs/InGaAs Heterostructure FET's Processed with Selective Reactive-Ion Etching Gate-Recess Technology" IEEE Electron Device Letters, Vol. 14, No. 9 pp 425-427 Sept 1993	
		K. SHINODA, A. TAIKE, H.SATO, AND H. UCHIYAMA, "Highly Reliable InGaAsP/InP Lasers with Defect-Free Regrowth Interfaces Formed by Newly Composed HBr-Based Solutions" 2001 International Conference on Indium Phosphide and Related Mat. pp 409-412 May 2001	

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